

**AMENDMENTS TO THE SPECIFICATION**

Please replace paragraph [0028] with the following amended paragraph:

[0028] For example, problems arise in that picture images can be scanned only in an originally designed direction. In other words, the output fails to be generated in the order of the last block to the first block. In this case, it is difficult to freely set a direction of the LCD panel, for example, a landscape or portrait type orientation. Because the system manufacturers [[manufactures]] who make the products that have LCD panels has a LCD panel want a display device capable of scanning images in more than one direction, this is a serious limitation of the related art LCD panels.

Please replace paragraph [0065] with the following amended paragraph:

[0065] A first capacitor C1 is connected between the drain of the first p-MOS transistor TFT1 and the Vss terminal. A contact node between the drain of the second p-MOS transistor TFT2 and the source of the third p-MOS transistor TFT3 is grounded through a first capacitor C1. The gate of the sixth p-MOS transistor TFT6 is connected to the Vss terminal through a second capacitor C2 and the gate and drain of TFT6 are connected with each other through a third capacitor C3. The gate of the seventh p-MOS transistor TFT7 is connected to the Vss terminal through a fourth capacitor C4.